

**UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY, HYDERABAD**

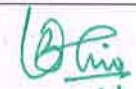
M.E. (ECE, EE, Mechanical) I – Semesters (Main) Examination, March/April 2015

REVISED EXAMINATION TIME TABLE

VASAVI COLLEGE OF ENGINEERING

Time: 2.00 PM To 5.00 PM

Date & Day	ELECTRONICS AND COMMUNICATION ENGINEERING		E. E. E.	MECHANICAL ENGINEERING
	Embedded Systems and VLSI Design	Communication Engg. & Signal Processing	Power Systems & Power Electronics	Advanced Design & Manufacturing
	Regular I - Semester	Regular I - Semester	Regular I - Semester	Regular I - Semester
23-03-2015 MONDAY	Physics of Semiconductor Devices	Coding Theory & Techniques	=====	=====
25-03-2015 WEDNESDAY	=====	Signal Compression Theory and Methods	=====	=====
27-03-2015 FRIDAY	Advanced Computer Organization	Multirate Processing	=====	=====
30-03-2015 MONDAY	=====	Digital Modulation Techniques	=====	=====
01-04-2015 WEDNESDAY	VLSI Technology	=====	=====	Finite Element Techniques
04-04-2015 SATURDAY	=====	=====	Advanced Synchronous Machine Theory	=====
06-04-2015 MONDAY	=====	Array Signal Processing	=====	=====
08-04-2015 WEDNESDAY	=====	Data and Computer Communication Networks	=====	=====
10-04-2015 FRIDAY	Analog and Mixed Signal IC Design	=====	=====	=====


 DIRECTOR OF EVALUATION
EXAMINATION CELL